

Figure 1a

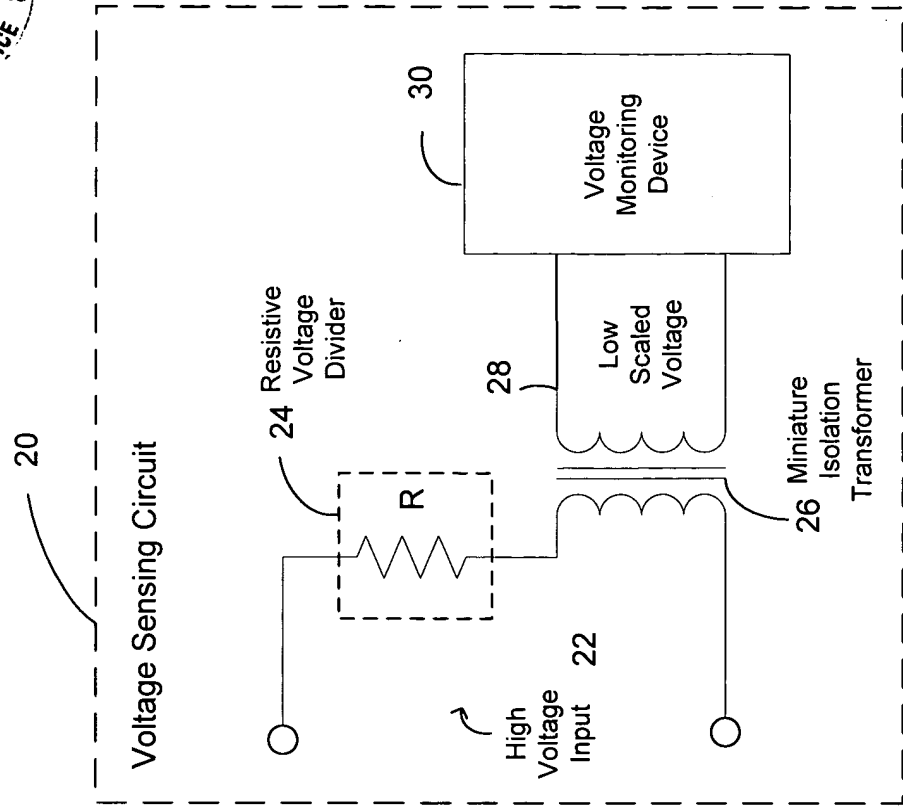


Figure 1b

PRIOR ART

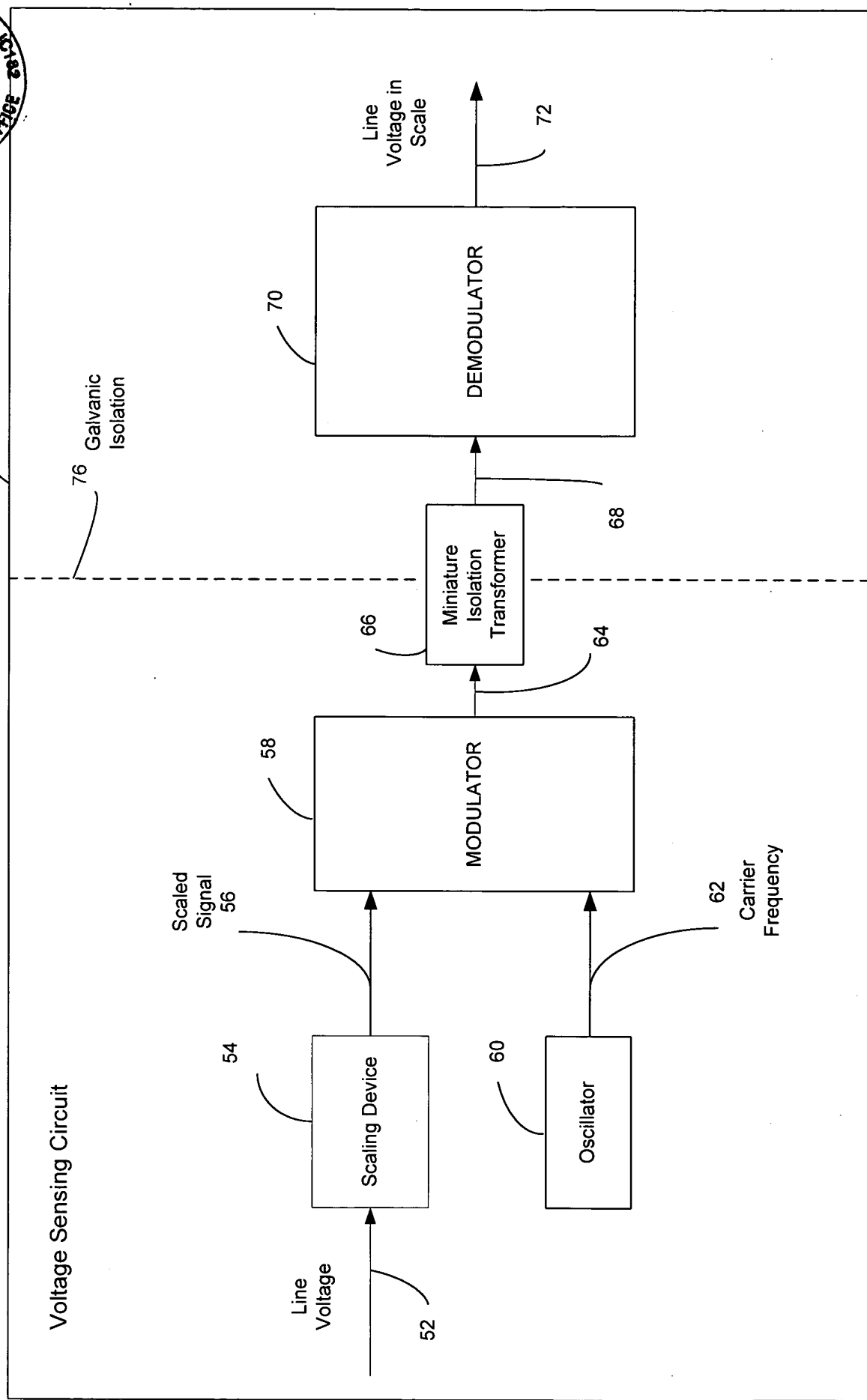
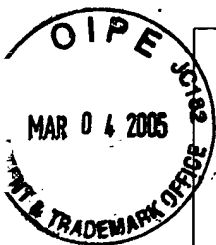
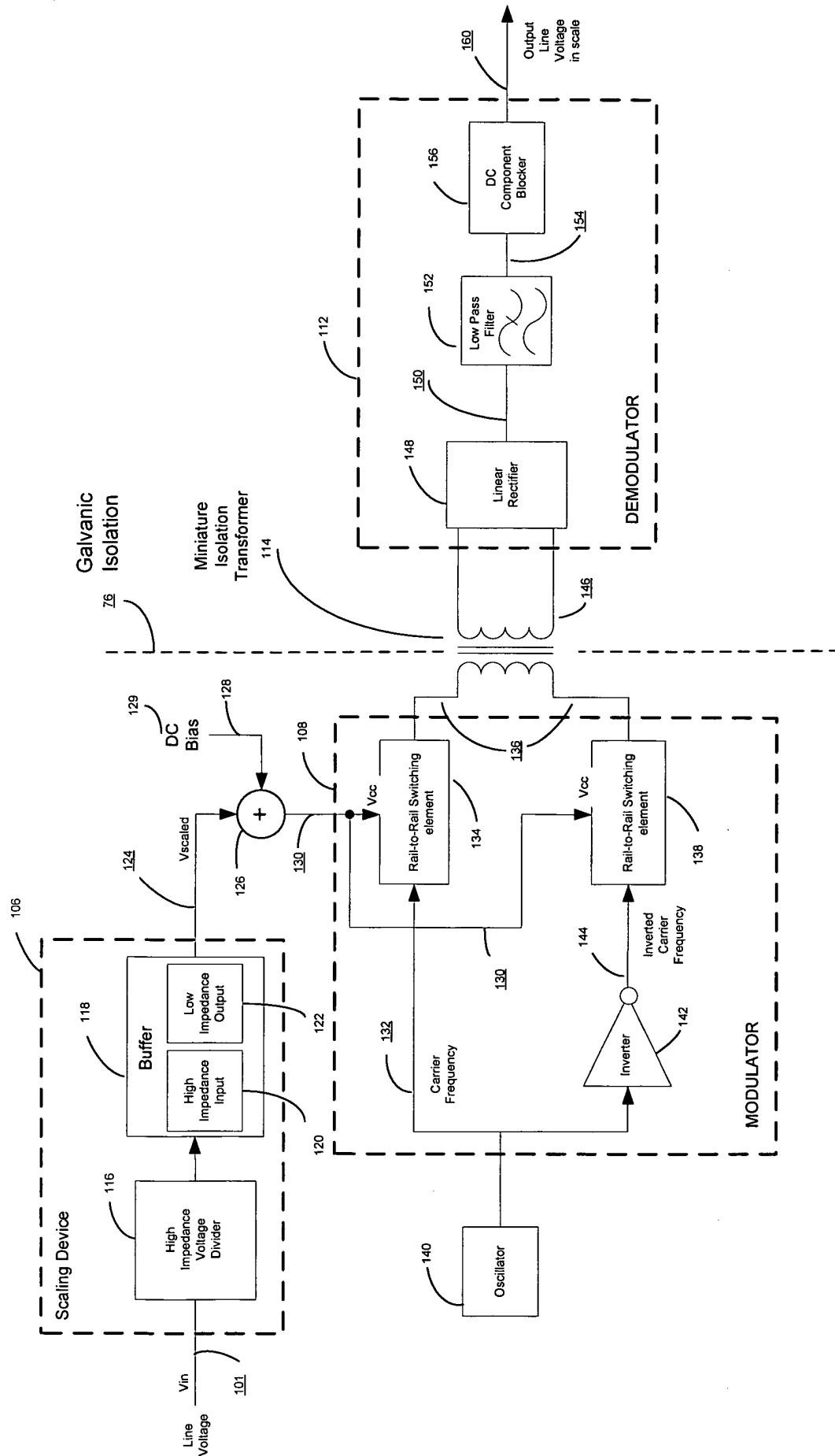


Figure 2

100

Secondary Side

Primary Side



Voltage Sensing Circuit

Figure 3

Fig. 4(a)

Line
Input
101

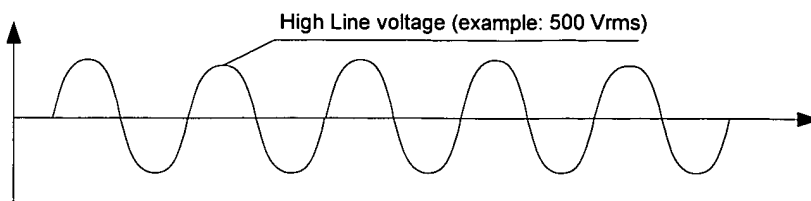


Fig. 4(b)

Scaled
Input
Signal
124

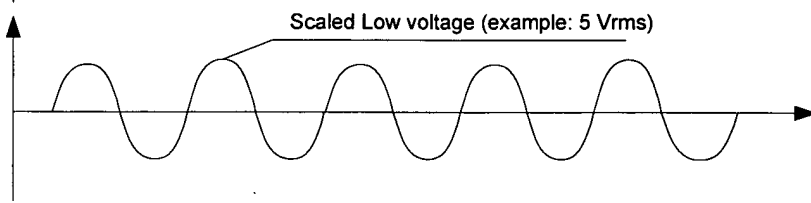


Fig. 4(c)

Carrier
Frequency
132

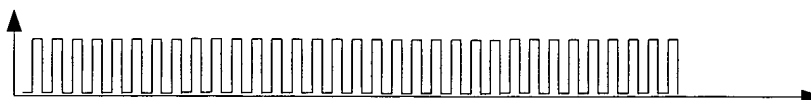


Fig. 4(d)

V_{cc}
130

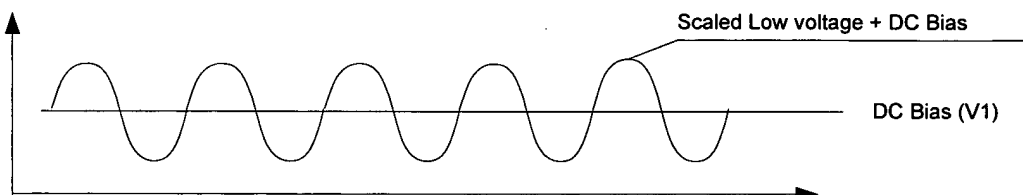


Fig. 4(e)

Modulator
and
Transformer
Output
136 and 146

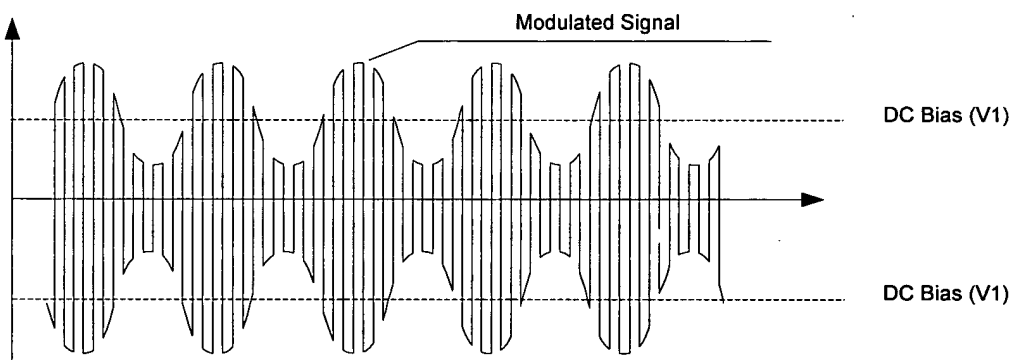


Fig. 4(f)

Linear
Rectifier
Output
150

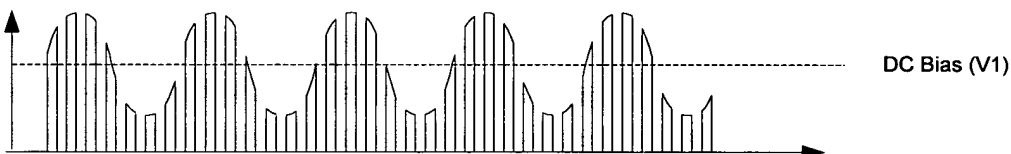


Fig. 4(g)

Low
Pass
Filter
Output
154

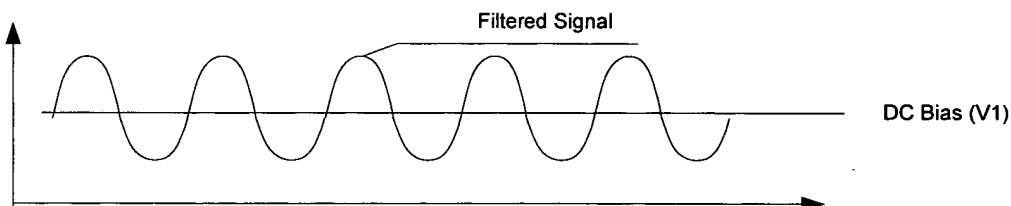


Fig. 4(h)

Output
Line
Voltage
in scale
160

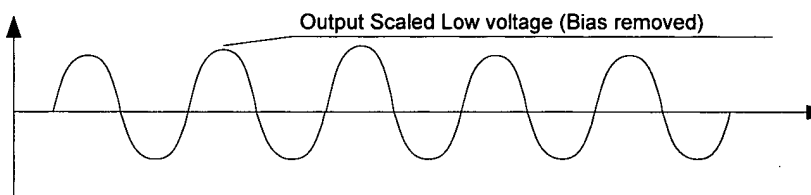


Figure 4 (a-h)

200

Galvanic Isolation

Voltage Sensing Circuit

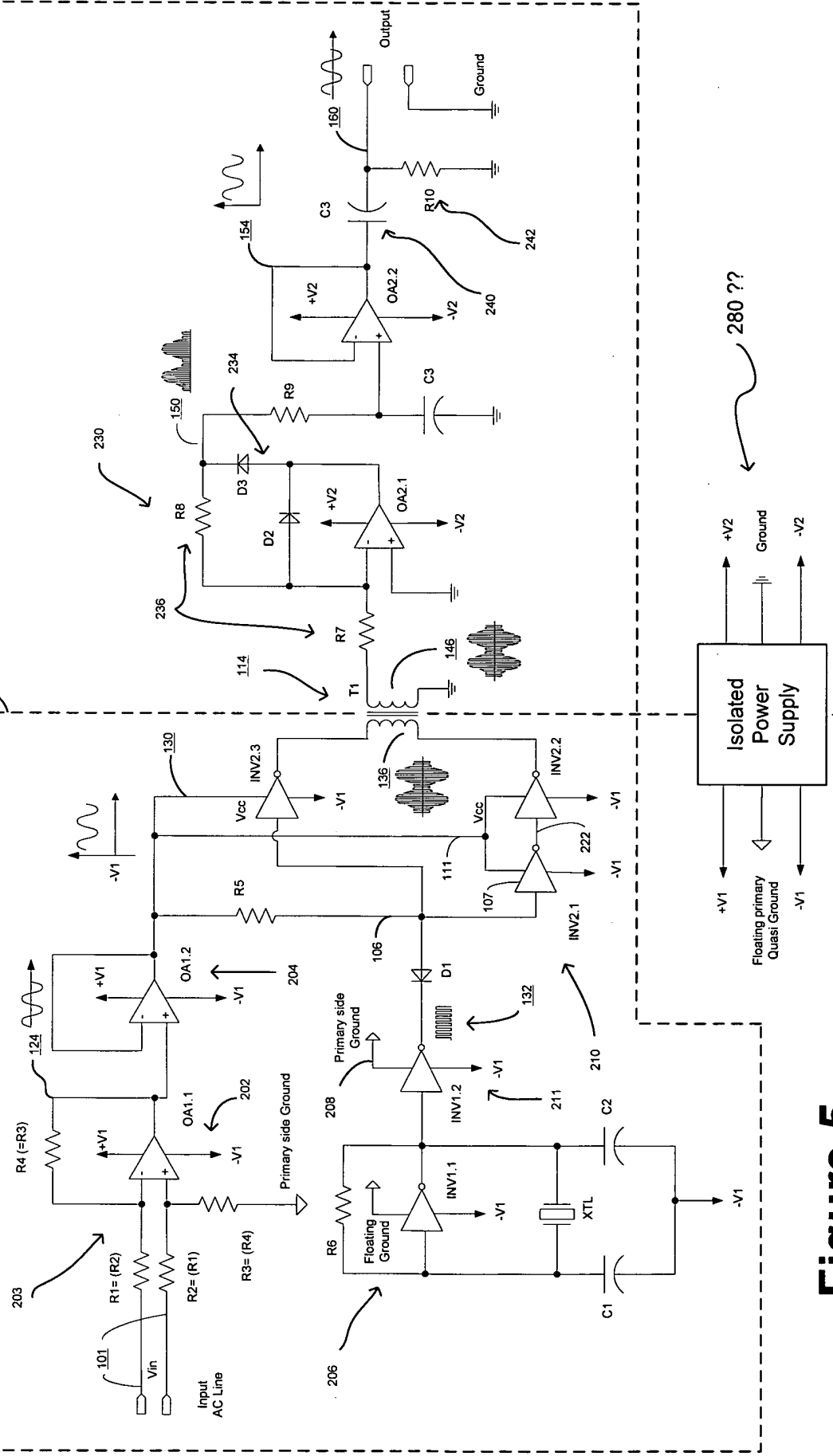


Figure 5

260

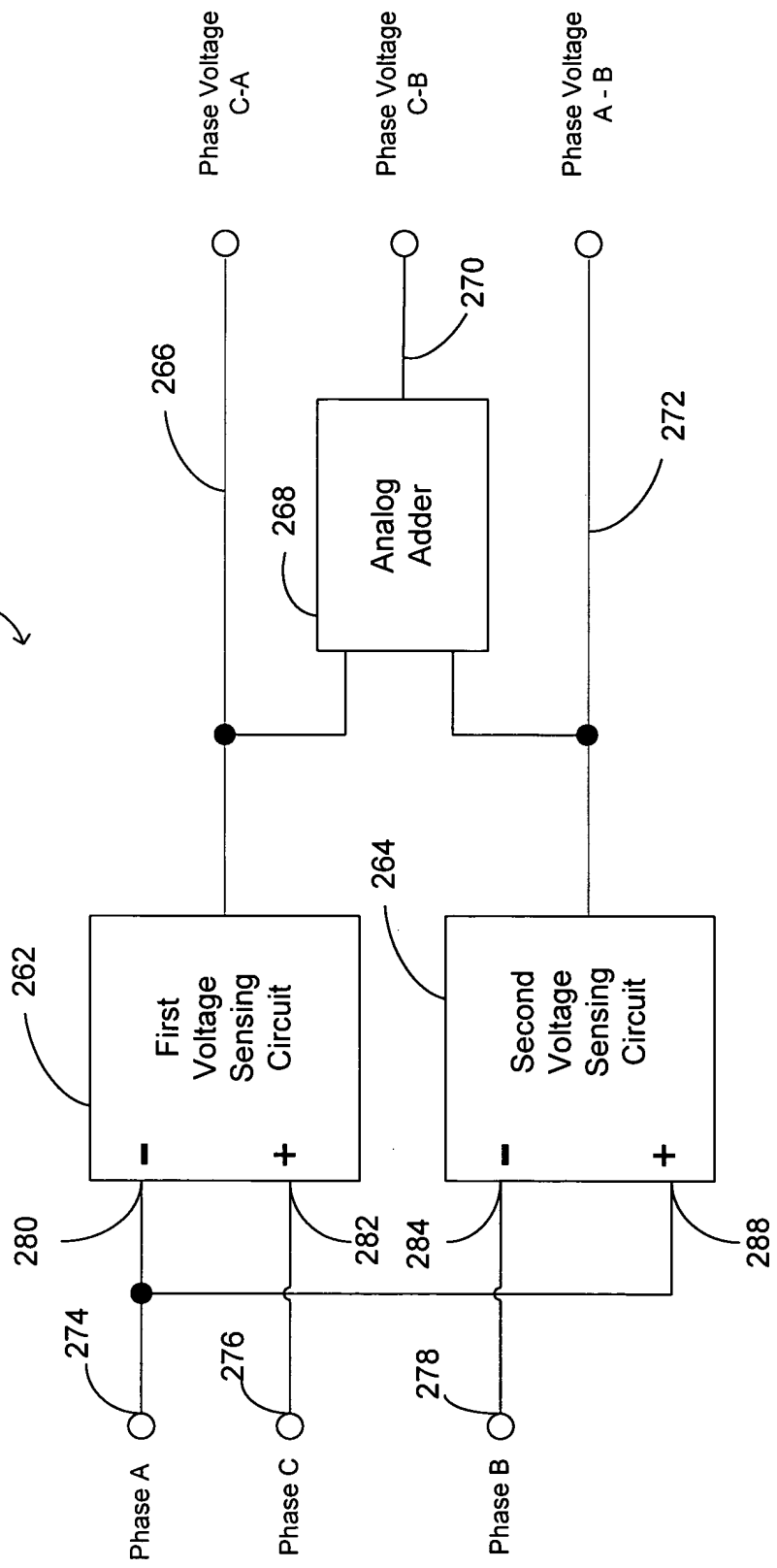


Figure 6

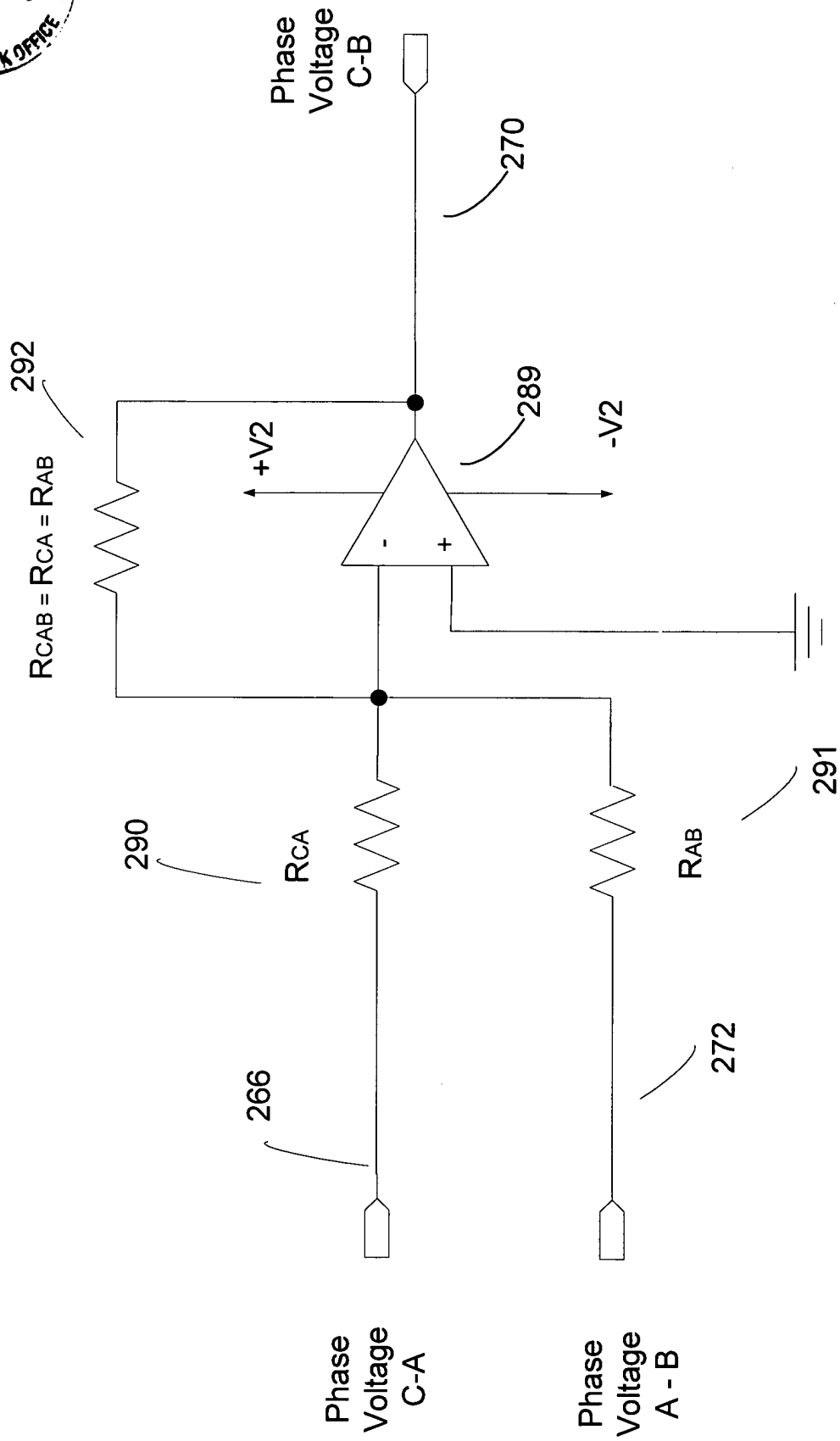


Figure 7